

08/08/97

UTILITY
SERIAL
NUMBER

PATENT DATE

PATENT
NUMBER

SERIAL NUMBER
08/984,562

FILING DATE
12/03/97

CLASS
711

SUBCLASS
105

GROUP ART UNIT
2751

EXAMINER
H. King

APPLICANTS

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CONTINUING DATA***

VERIFIED THIS APPLN IS A DIV OF 08/650,719 05/20/96

yes uh

FOREIGN APPLICATIONS***
VERIFIED

None uh

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FOREIGN FILING LICENSE GRANTED 03/16/98

Foreign priority claimed
35 USC 119 conditions met

☐ yes ☒ no
☐ yes ☒ no

AS
FILED

STATE OR
COUNTRY

SHEETS
DRWGS.

TOTAL
CLAIMS

INDEP.
CLAIMS

FILING FEE
RECEIVED

ATTORNEY'S
DOCKET NO.

Verified and Acknowledged

Examiner's Initials

→

ID

20

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\$790.00

95-0653.02

W ERIC WEBSTAD

MICRON TECHNOLOGY, INC.

3000 S FEDERAL WAY, Mail Stop 525

BOISE ID 83706 - 9632

2/1/86

BURST PIPELINED EDO MEMORY DEVICE

Pipelined Operation with Mode Selection Circuitry

U.S. DEPT. OF COMM./PAT. & TM—PTO-436L (Rev.12-94)

PARTS OF APPLICATION
FILED SEPARATELY

Applications Examiner

NOTICE OF ALLOWANCE MAILED

CLAIMS ALLOWED

Total Claims

Print Claim

Assistant Examiner

ISSUE FEE

DRAWING

Amount Due

Date Paid

Sheets Drwg.

Figs. Drwg.

Print Fig.

Label
Area

Primary Examiner

PREPARED FOR ISSUE

ISSUE
BATCH
NUMBER

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Form PTO-436A
(Rev. 8/92)

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